

<b>Course Number and Name</b>												
BEC6L2 - ELECTRONICS SYSTEM DESIGN LAB												
<b>Credits and Contact Hours</b>												
2 and 45												
<b>Course Coordinator's Name</b>												
Dr E.Kanniga												
<b>Text Books and References</b>												
LAB MANUAL												
<b>Course Description</b>												
<ul style="list-style-type: none"> <li>To understand the design procedure of different power supplies.</li> <li>To know to design transreceiver and voltage regulator.</li> <li>To understand the working of Microprocessor and DSP based system design</li> </ul>												
<b>Prerequisites</b>						<b>Co-requisites</b>						
Electronics Circuits and Communication engineering I Lab						Control system						
required, elective, or selected elective (as per Table 5-1)												
Required												
<b>Course Outcomes (COs)</b>												
CO1: Design different forms of power supply.												
CO2: Design Voltage regulators												
CO3: AM/FM transreceiver.												
CO4: Know the design procedure of Instrumentation amplifier and Digital Indicator.												
CO5: Learn CAD based PCB layout design.												
CO6: Understand the working of modems and timers.												
<b>Student Outcomes (SOs) from Criterion 3 covered by this Course</b>												
	COs/SOs	a	b	c	d	e	f	g	h	i	j	k
	CO1	H					M					
	CO2	M	L	H				H	H	L	H	
	CO3	M	H		H		M		L	M	M	H
	CO4	M	H		M	H			M	M		
	CO5	M	M	M		H	M	M				
	CO6	M			M		H				M	M
<b>List of Topics Covered</b>												
<ol style="list-style-type: none"> <li>Design of high current linear variable DC Power supply.</li> <li>Design of Switched Mode power supply.</li> <li>Design of AC / DC Voltage regulator using SCR.</li> <li>Design of Programmable Logic controller.</li> <li>Design of process control timer.</li> <li>Design of AM / FM transreceiver</li> <li>Design of wireless data Modems</li> <li>Design of Instrumentation amplifier and Digital Indicator</li> <li>PCB layout Design using CAD</li> <li>Microprocessor based system design.</li> <li>DSP based system design.</li> </ol>												